

Case No. 02GR126654484

MS PATENT APPLICATION
COMMISSIONER FOR PATENTS
PO BOX 1450
ALEXANDRIA, VA 22313-1450

I HEREBY CERTIFY THIS PAPER OR FEE IS BEING
DEPOSITED WITH THE U.S. POSTAL SERVICE
"EXPRESS MAIL POST OFFICE TO ADDRESSEE"
SERVICE UNDER 37 CFR 1.10 ON THE DATE
INDICATED BELOW AND IS ADDRESSED TO: MS
PATENT APPLICATION, COMMISSIONER FOR
PATENTS, PO BOX 1450, ALEXANDRIA, VA 22313-
1450.

EXPRESS MAIL NO: EV301516244US

DATE OF DEPOSIT: Nov mber 14, 2003

NAME: Justin Goree

SIGNATURE: Justin Goree

Transmitted herewith for filing is the patent
application of:

Inv ntors: **Damien LENOBLE**

For: **PROCESS FOR FABRICATING A SHORT-GATE-LENGTH MOS TRANSISTOR AND
INTEGRATED CIRCUIT COMPRISING SUCH A TRANSISTOR**

Enclosed are:

- ☒ Patent Application: 16 pages, 24 claims
5 sheets of drawings
The suggested drawing figure to be published is FIG. 8.
Citation Under 37 CFR 1.97 and PTO-1449.
A Preliminary Amendment.
Assignee info:
Name: STMicronics SA
Address: 29, boulevard Romain Rolland, F-92120 Montrouge, France
Country of Incorporation: France
☒ Applicant claims priority benefit to the following foreign application(s):
Country: France
Application No.: 0214255
Filing Date: November 14, 2002

The Declaration and Filing Fee are **NOT ENCLOSED**.

- ☒ Name, Address and Citizenship of Inventor(s) is as follows:

Damien LENOBLE
11, rue des Palettes
38610 Gieres, France
Citizen of France

- ☒ **PLEASE ADDRESS CORRESPONDENCE TO ATTORNEY OF RECORD: CHRISTOPHER F. REGAN**

- ☒ Associate this file with Customer No. 27975.



27975

PATENT TRADEMARK OFFICE

Date: November 14, 2003

Michael W. Taylor
MICHAEL W. TAYLOR
Reg. No. 43,182

22386 U.S. PTO
10/714440

